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<u>L5</u>	L3 AND (precompiler OR preprocessor OR pre-processor).ab.	1	<u>L5</u>
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<u>L3</u>	L2 AND 717/\$\$\$ccls.	149	<u>L3</u>
<u>L2</u>	(directive OR probe) and (optimize OR optimization OR instrumentation)	19816	<u>L2</u>
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<u>L9</u>	Common ADJ Runtime ADJ language	0	<u>L9</u>
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<u>L5</u>	L3 AND (precompiler OR preprocessor OR pre-processor).ab.	1	<u>L5</u>
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... Supports **ACPI**, WFM 2.0. ... System BIOS: 2Mb, 4Mb or 8Mb with boot block flash ROM in FWH. (Default 4 MB.) PC99 and PnP/**ACPI** compatible. ...

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DG MAJORS - 2003 - umr.edu

... To ensure that the necessary include file data was present, the code was processed through the C **preprocessor** using the Linux include directory. ...

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R Minnich - Linux Journal, 2005 - portal.acm.org

... found in the \$PIR (uniprocessor), **_MP_** (multiprocessor or IO-APIC) or **ACPI** tables. ...

Notice that in the style of the Linux kernel, C **preprocessor**-enabled code is ...

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MG PHILIP - INTERNATIONAL JOURNAL FOR NUMERICAL METHODS IN FLUIDS, 1984 - doi.wiley.com

... is a diagonal matrix; furthermore, it need only be formed 'once per problem' since it is constant, and this is conveniently done in a **preprocessor** code. ...

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LR Leung, Y Qian, X Bian, WM Washington, J Han, JO ... - Climatic Change, 2004 - Springer

... SSJ) River Basin, which are the study regions of the **ACPI** impact assessment. ... The

MM5 **preprocessor** was modified to convert PCM outputs to create initial and ...

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Y He, CHQ Ding - International Journal of High Performance Computing ..., 2005 - hpc.sagepub.com

... a standalone version of the component, sufficient modifications (such as **preprocessor** **ifdef**) need ... examples available online; <http://hpcrd.lbl.gov/SCG/acpi/> MPH ...

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[book] An Annotated Bibliography on the Construction of Compilers

BW Pollack - 1971 - reports.stanford.edu
... Hath, Logik 8 (19621, 299-308. (German), t language # Perlis, AJ The synthesis of
algorithmic systems. 3 **ACPI** 14, 1 (Jan 1967), I-9. Ic compiling # Page 14. ...
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Y He, CHQ Ding - International Journal of High Performance Computing ..., 2005 - hpc.sagepub.com

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1 [Porting LinuxBIOS to the AMD SC520](#)

Ron Minnich

 August 2005 **Linux Journal**, Volume 2005 Issue 136

Publisher: Specialized Systems Consultants, Inc.

 Full text available: [html\(44.93 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

LinuxBIOS doesn't just boot fast. Other advantages include a fallback copy and the ability to maintain BIOS code in C.

2 [A Fortran preprocessor for the large program environment](#)



Neal R. Wagner

 December 1980 **ACM SIGPLAN Notices**, Volume 15 Issue 12

Publisher: ACM Press

 Full text available: [pdf\(902.71 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The use of a preprocessor to aid structured programming in Fortran has been widely discussed. This article considers a design philosophy which is especially oriented toward large program development and maintenance. The design is distinguished by the retention of the form of the original source program in the standard Fortran output by the preprocessor. A specific implementation is described.

3 [A programmable preprocessor for parallelizing Fortran-90](#)



Matt Rosing

 January 1999 **Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)**

Publisher: ACM Press

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4 [Ada as a preprocessor language](#)



P. L. Baker

 January 1990 **ACM SIGAda Ada Letters**, Volume X Issue 1

Publisher: ACM Press

 Full text available: [pdf\(649.14 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Preprocessors are components of a software development environment that can increase productivity by providing semantic capabilities for expressing certain source language

statement s concisely and directly which would otherwise be expressed indirectly and verbosely. Moreover, it is frequently necessary to reflect a single programming decision in several places in the source text; a preprocessor can propagate a single specification of such a decision to the points it affects thereby reducing eff ...

5 A preprocessor for structural analysis programs



Peter K. Ho

June 1976 **Proceedings of the 13th conference on Design automation**

Publisher: ACM Press

Full text available: [pdf\(589.56 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This preprocessor generates and updates input data on the geometry and properties of a structure and its foundation, and on gravity, seismic and other loadings.

6 The application of JavaCC to develop a C/C++ preprocessor



Giancarlo Succi, Raymond W. Wong

September 1999 **ACM SIGAPP Applied Computing Review**, Volume 7 Issue 3

Publisher: ACM Press

Full text available: [pdf\(444.37 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

The commonly available software metrics-extraction tools for C/C++ depend on commercial preprocessors to preprocess the source file before being input into the analyzers. The following paper introduces a Java compiler generator called JavaCC and the application of the generator to develop a Java-based preprocessor for C/C++. Some technical features to the development of preprocessor are also mentioned, such as (1) handling of rescanning in preprocessing with LL(k) parsers, (2) managing condition ...

7 A structured APL preprocessor



Michael L. Cook, Mark G. Arnold

May 1981 **ACM SIGPLAN Notices**, Volume 16 Issue 5

Publisher: ACM Press

Full text available: [pdf\(585.98 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

This paper presents a set of structured control statements for APL and a preprocessor to implement them. The preprocessor translates structured APL functions into APL functions using the branch operator to replace the structured statements. The translation is based on finding keywords, such as IF and WHILE, appearing in syntactically valid places in the function. Since no modification of either the APL interpreter or APL syntax is required, the APL editor can be used to modify structured functio ...

8 Design and implementation of PL/I preprocessor-based systems



B. M. Schwartz

September 1972 **ACM SIGPLAN Notices**, Volume 7 Issue 9

Publisher: ACM Press

Full text available: [pdf\(747.70 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper describes and illustrates the type of simple application-oriented "language" that is easily implemented as a set of PL/I %procedure calls. A tool kit of % procedures designed to simplify the task is described. Finally, a general approach to the design and implementation of this type of system is discussed.

9 Automatic generation of graphic displays of data structures through a preprocessor



Moshe Augenstein, Yedidyah Langsam

February 1988 **ACM SIGCSE Bulletin , Proceedings of the nineteenth SIGCSE technical symposium on Computer science education SIGCSE '88**, Volume 20 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(553.68 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Recent attention has been given to graphic display routines that allow the programmer to observe the effects of applications programs on various data structures. Much of the work reported in the literature has involved the animation of specific algorithms and has necessitated manual effort by programmers on an application by application basis. Results of initial work in developing a general purpose tool for the display of data structures have already been published. In order to make ...

10 Software in the spotlight: FPP, a new implementation of an old preprocessor



August 1996 **ACM SIGPLAN Fortran Forum**, Volume 15 Issue 2

Publisher: ACM Press

Full text available:  [pdf\(260.62 KB\)](#) Additional Information: [full citation](#), [index terms](#)


11 A generalized graphic preprocessor for two-dimensional finite element analysis



Robert Haber, Mark Shephard, John Abel, Richard Gallagher, Donald Greenberg

August 1978 **ACM SIGGRAPH Computer Graphics , Proceedings of the 5th annual conference on Computer graphics and interactive techniques SIGGRAPH '78**, Volume 12 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(1.98 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Input preprocessors have come to be recognized as important components of modern finite element programs. A method is described which utilizes interactive computer graphics digitizing techniques to create a powerful input preprocessor for finite element analysis. A limited number of general mesh generators based on linear blending functions permit the program to handle virtually all two-dimensional topologies. The processes of geometric input and specification of problem-specific "at ...

Keywords: Computer graphics, Finite element preprocessing, Mesh generation, Structural analysis


12 A preprocessor for channel routing



Ming H. Young, Larry Cooke

June 1981 **Proceedings of the 18th conference on Design automation**

Publisher: IEEE Press

Full text available:  [pdf\(279.45 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents a "preprocessor" which separates a channel routing problem into two subproblems. One is a specialized channel routing problem where no two nodes of two different nets are of the same y-grid position. The other is a problem of connecting pairs of nodes where each pair of nodes has a path reserved for it. The use of a "preprocessor" in channel routing[5] is justified by the comparison of routing results.

13 Description of basic algorithm DETAB/65 preprocessor



Michael D. Callahan, Anson E. Chapman

July 1967 **Communications of the ACM**, Volume 10 Issue 7

Publisher: ACM Press

Full text available:  [pdf\(732.97 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The basic algorithm for the conversion of decision tables into COBOL code is contained in

the generator portion of the DETAB/65 preprocessor. The generator analyzes a decision table and produces simple COBOL conditional statements. Core storage is saved by using queueing techniques and extensive indexing and also by outputting the code as it is generated, a line at a time. The only optimization attempted is the elimination of obviously unnecessary tests on certain conditions in the decision ...

14 Modification and extension to a fortran preprocessor (RATFOR) for compatibility with the Xerox CP-V operating system



John R. Suber

April 1978 **Proceedings of the 16th annual Southeast regional conference**

Publisher: ACM Press

Full text available: [pdf\(242.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

After being introduced to RATFOR, a structured FORTRAN preprocessor at the University of California, Irvine Campus in 1977, the author became impressed with its potential and obtained a copy for use at the University of Southern Mississippi. Upon returning to U.S.M. in September 1977 an initial copy of the preprocessor was installed by the author and checked out in September of 1977.

15 Use of preprocessor as a tool to assist students in implementing stacks and queues



Thomas E. Gerasch

March 1985 **ACM SIGCSE Bulletin , Proceedings of the sixteenth SIGCSE technical symposium on Computer science education SIGCSE '85**, Volume 17 Issue 1

Publisher: ACM Press

Full text available: [pdf\(385.51 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

16 A macro preprocessor for the simulation language network II.5



David J. Thuyente, Robert L. Sedlmeyer

April 1990 **ACM SIGSIM Simulation Digest , Proceedings of the 23rd annual symposium on Simulation ANSS '90**, Volume 20 Issue 4

Publisher: IEEE Press, ACM Press

Full text available: [pdf\(655.33 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Network II.5 [Garrison 1989] is a powerful simulation tool for the design and analysis of multiprocessor systems, but lacks features to support modelling-in-the-large. We have developed a macro preprocessor, NETPRE, to aid in the construction and maintenance of large simulation models. NETPRE supports symbolic constants, include files, and macros. We show how these features can reduce model development time, increase the readability of model descriptions, and facilitate experimentation.

17 Software engineering: applications, practices and tools (SE): poster papers: Bridging AOP to SMP: turning GCC into a metalanguage preprocessor



Tiago Stein D'Agostini, Antônio Augusto Fröhlich

March 2005 **Proceedings of the 2005 ACM symposium on Applied computing SAC '05**

Publisher: ACM Press

Full text available: [pdf\(116.81 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This article presents an strategy to combine important software engineering techniques, *Static Metaprogramming* (SMP) and *generic Programming* (GP) with *Aspect Oriented Programming* (AOP). These rely on specific language tools that, today, cannot be deployed in conjunction, thus imposing limitations on the software development process. Our strategy consists in adapting the C++ compiler to act as a SMP preprocessor. This preprocessor is able to parse the input program, execute e ...

Keywords: aspects, metaprogramming

18 Program summaries: Preprocessors for noisy speech ☐

George Zweig

October 1989 **Proceedings of the workshop on Speech and Natural Language HLT '89****Publisher:** Association for Computational LinguisticsFull text available:  [pdf\(61.08 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The objective of this project is to develop a preprocessor for speech recognition systems operating in noisy environments. The preprocessor, consisting of a nonlinear inhomogeneous transmission line, will be realized in software, although realization in hardware in FY91 should be possible. More specifically we will: 1) Develop a nonlinear transmission line preprocessor that accurately simulates the mechanics of the mammalian inner ear at all sound pressure levels. 2) Preprocess speech with the non ...

19 Technical papers: Signition: A preprocessor for speech recognition systems operating in noisy environments ☐

George Zweig

February 1989 **Proceedings of the workshop on Speech and Natural Language HLT '89****Publisher:** Association for Computational LinguisticsFull text available:  [pdf\(57.69 KB\)](#) Additional Information: [full citation](#), [abstract](#)

The recognition of speech in noisy environments is critical to certain DoD systems now under development. Current preprocessors for speech recognition systems, such as those based on "linear predictive coding," are linear and therefore not effective in noisy environments. The objective of this project is to develop a nonlinear preprocessor for speech recognition systems that significantly improves the signal to noise ratio of the speech signal to be recognized. The nonlinear transmission line wi ...

20 Increased productivity using a preprocessor for Dataflex Fourth Generation Database ☐**Language (abstract)**

Herbert E. Longenecker, S. Tariq Ali, Michael V. Doran

January 1990 **Proceedings of the 1990 ACM annual conference on Cooperation****Publisher:** ACM PressFull text available:  [pdf\(70.48 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Reusable code and higher generation languages have been sited as methods for systems development which will cause reduction of time to develop new code, and which will lead to significant reduction of developmental errors. In each generation of language syntactic elements of the language decompose into multiple statements at a lower level. Dataflex is a 4th GL associated with a hierarchical database. While it is an efficient language, it nonetheless consumes considerable numbers ...

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1 [Porting LinuxBIOS to the AMD SC520](#)

Ron Minnich

 August 2005 **Linux Journal**, Volume 2005 Issue 136

Publisher: Specialized Systems Consultants, Inc.

 Full text available: [html\(44.93 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

LinuxBIOS doesn't just boot fast. Other advantages include a fallback copy and the ability to maintain BIOS code in C.

2 [A Fortran preprocessor for the large program environment](#)



Neal R. Wagner

 December 1980 **ACM SIGPLAN Notices**, Volume 15 Issue 12

Publisher: ACM Press

 Full text available: [pdf\(902.71 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The use of a preprocessor to aid structured programming in Fortran has been widely discussed. This article considers a design philosophy which is especially oriented toward large program development and maintenance. The design is distinguished by the retention of the form of the original source program in the standard Fortran output by the preprocessor. A specific implementation is described.

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4 [Ada as a preprocessor language](#)



P. L. Baker

 January 1990 **ACM SIGAda Ada Letters**, Volume X Issue 1

Publisher: ACM Press

 Full text available: [pdf\(649.14 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

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Peter K. Ho

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Giancarlo Succi, Raymond W. Wong

September 1999 **ACM SIGAPP Applied Computing Review**, Volume 7 Issue 3

Publisher: ACM Press

Full text available: [pdf\(444.37 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

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Michael L. Cook, Mark G. Arnold

May 1981 **ACM SIGPLAN Notices**, Volume 16 Issue 5

Publisher: ACM Press

Full text available: [pdf\(585.98 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

This paper presents a set of structured control statements for APL and a preprocessor to implement them. The preprocessor translates structured APL functions into APL functions using the branch operator to replace the structured statements. The translation is based on finding keywords, such as IF and WHILE, appearing in syntactically valid places in the function. Since no modification of either the APL interpreter or APL syntax is required, the APL editor can be used to modify structured functio ...

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September 1972 **ACM SIGPLAN Notices**, Volume 7 Issue 9

Publisher: ACM Press

Full text available: [pdf\(747.70 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper describes and illustrates the type of simple application-oriented "language" that is easily implemented as a set of PL/I %procedure calls. A tool kit of % procedures designed to simplify the task is described. Finally, a general approach to the design and implementation of this type of system is discussed.

9 Automatic generation of graphic displays of data structures through a preprocessor



Moshe Augenstein, Yedidyah Langsam

February 1988 **ACM SIGCSE Bulletin , Proceedings of the nineteenth SIGCSE technical symposium on Computer science education SIGCSE '88**, Volume 20 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(553.68 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Recent attention has been given to graphic display routines that allow the programmer to observe the effects of applications programs on various data structures. Much of the work reported in the literature has involved the animation of specific algorithms and has necessitated manual effort by programmers on an application by application basis. Results of initial work in developing a general purpose tool for the display of data structures have already been published. In order to make ...

10 Software in the spotlight: FPP, a new implementation of an old preprocessor



August 1996 **ACM SIGPLAN Fortran Forum**, Volume 15 Issue 2

Publisher: ACM Press

Full text available:  [pdf\(260.62 KB\)](#) Additional Information: [full citation](#), [index terms](#)

11 A generalized graphic preprocessor for two-dimensional finite element analysis



Robert Haber, Mark Shephard, John Abel, Richard Gallagher, Donald Greenberg

August 1978 **ACM SIGGRAPH Computer Graphics , Proceedings of the 5th annual conference on Computer graphics and interactive techniques SIGGRAPH '78**, Volume 12 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(1.98 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Input preprocessors have come to be recognized as important components of modern finite element programs. A method is described which utilizes interactive computer graphics digitizing techniques to create a powerful input preprocessor for finite element analysis. A limited number of general mesh generators based on linear blending functions permit the program to handle virtually all two-dimensional topologies. The processes of geometric input and specification of problem-specific "at ...

Keywords: Computer graphics, Finite element preprocessing, Mesh generation, Structural analysis


12 A preprocessor for channel routing



Ming H. Young, Larry Cooke

June 1981 **Proceedings of the 18th conference on Design automation**

Publisher: IEEE Press

Full text available:  [pdf\(279.45 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents a "preprocessor" which separates a channel routing problem into two subproblems. One is a specialized channel routing problem where no two nodes of two different nets are of the same y-grid position. The other is a problem of connecting pairs of nodes where each pair of nodes has a path reserved for it. The use of a "preprocessor" in channel routing[5] is justified by the comparison of routing results.


13 Description of basic algorithm DETAB/65 preprocessor



Michael D. Callahan, Anson E. Chapman

July 1967 **Communications of the ACM**, Volume 10 Issue 7

Publisher: ACM Press

Full text available:  [pdf\(732.97 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The basic algorithm for the conversion of decision tables into COBOL code is contained in

the generator portion of the DETAB/65 preprocessor. The generator analyzes a decision table and produces simple COBOL conditional statements. Core storage is saved by using queueing techniques and extensive indexing and also by outputting the code as it is generated, a line at a time. The only optimization attempted is the elimination of obviously unnecessary tests on certain conditions in the decision ...

14 Modification and extension to a fortran preprocessor (RATFOR) for compatibility with the Xerox CP-V operating system



John R. Suber

April 1978 **Proceedings of the 16th annual Southeast regional conference**

Publisher: ACM Press

Full text available: [pdf\(242.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

After being introduced to RATFOR, a structured FORTRAN preprocessor at the University of California, Irvine Campus in 1977, the author became impressed with its potential and obtained a copy for use at the University of Southern Mississippi. Upon returning to U.S.M. in September 1977 an initial copy of the preprocessor was installed by the author and checked out in September of 1977.

15 Use of preprocessor as a tool to assist students in implementing stacks and queues



Thomas E. Gerasch

March 1985 **ACM SIGCSE Bulletin , Proceedings of the sixteenth SIGCSE technical symposium on Computer science education SIGCSE '85**, Volume 17 Issue 1

Publisher: ACM Press

Full text available: [pdf\(385.51 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

16 A macro preprocessor for the simulation language network II.5



David J. Thuyente, Robert L. Sedlmeyer

April 1990 **ACM SIGSIM Simulation Digest , Proceedings of the 23rd annual symposium on Simulation ANSS '90**, Volume 20 Issue 4

Publisher: IEEE Press, ACM Press

Full text available: [pdf\(655.33 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Network II.5 [Garrison 1989] is a powerful simulation tool for the design and analysis of multiprocessor systems, but lacks features to support modelling-in-the-large. We have developed a macro preprocessor, NETPRE, to aid in the construction and maintenance of large simulation models. NETPRE supports symbolic constants, include files, and macros. We show how these features can reduce model development time, increase the readability of model descriptions, and facilitate experimentation.

17 Software engineering: applications, practices and tools (SE): poster papers: Bridging AOP to SMP: turning GCC into a metalanguage preprocessor



Tiago Stein D'Agostini, Antônio Augusto Fröhlich

March 2005 **Proceedings of the 2005 ACM symposium on Applied computing SAC '05**

Publisher: ACM Press

Full text available: [pdf\(116.81 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This article presents an strategy to combine important software engineering techniques, *Static Metaprogramming* (SMP) and *generic Programming* (GP) with *Aspect Oriented Programming* (AOP). These rely on specific language tools that, today, cannot be deployed in conjunction, thus imposing limitations on the software development process. Our strategy consists in adapting the C++ compiler to act as a SMP preprocessor. This preprocessor is able to parse the input program, execute e ...

Keywords: aspects, metaprogramming

18 Program summaries: Preprocessors for noisy speech ☐

George Zweig

October 1989 **Proceedings of the workshop on Speech and Natural Language HLT '89****Publisher:** Association for Computational LinguisticsFull text available:  [pdf\(61.08 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The objective of this project is to develop a preprocessor for speech recognition systems operating in noisy environments. The preprocessor, consisting of a nonlinear inhomogeneous transmission line, will be realized in software, although realization in hardware in FY91 should be possible. More specifically we will: 1) Develop a nonlinear transmission line preprocessor that accurately simulates the mechanics of the mammalian inner ear at all sound pressure levels. 2) Preprocess speech with the non ...

19 Technical papers: Signition: A preprocessor for speech recognition systems operating in noisy environments ☐

George Zweig

February 1989 **Proceedings of the workshop on Speech and Natural Language HLT '89****Publisher:** Association for Computational LinguisticsFull text available:  [pdf\(57.69 KB\)](#) Additional Information: [full citation](#), [abstract](#)

The recognition of speech in noisy environments is critical to certain DoD systems now under development. Current preprocessors for speech recognition systems, such as those based on "linear predictive coding," are linear and therefore not effective in noisy environments. The objective of this project is to develop a nonlinear preprocessor for speech recognition systems that significantly improves the signal to noise ratio of the speech signal to be recognized. The nonlinear transmission line wi ...

20 Increased productivity using a preprocessor for Dataflex Fourth Generation Database Language (abstract) ☐

Herbert E. Longenecker, S. Tariq Ali, Michael V. Doran

January 1990 **Proceedings of the 1990 ACM annual conference on Cooperation****Publisher:** ACM PressFull text available:  [pdf\(70.48 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Reusable code and higher generation languages have been sited as methods for systems development which will cause reduction of time to develop new code, and which will lead to significant reduction of developmental errors. In each generation of language syntactic elements of the language decompose into multiple statements at a lower level. Dataflex is a 4th GL associated with a hierarchical database. While it is an efficient language, it nonetheless consumes considerable numbers ...

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